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FIG. 1.

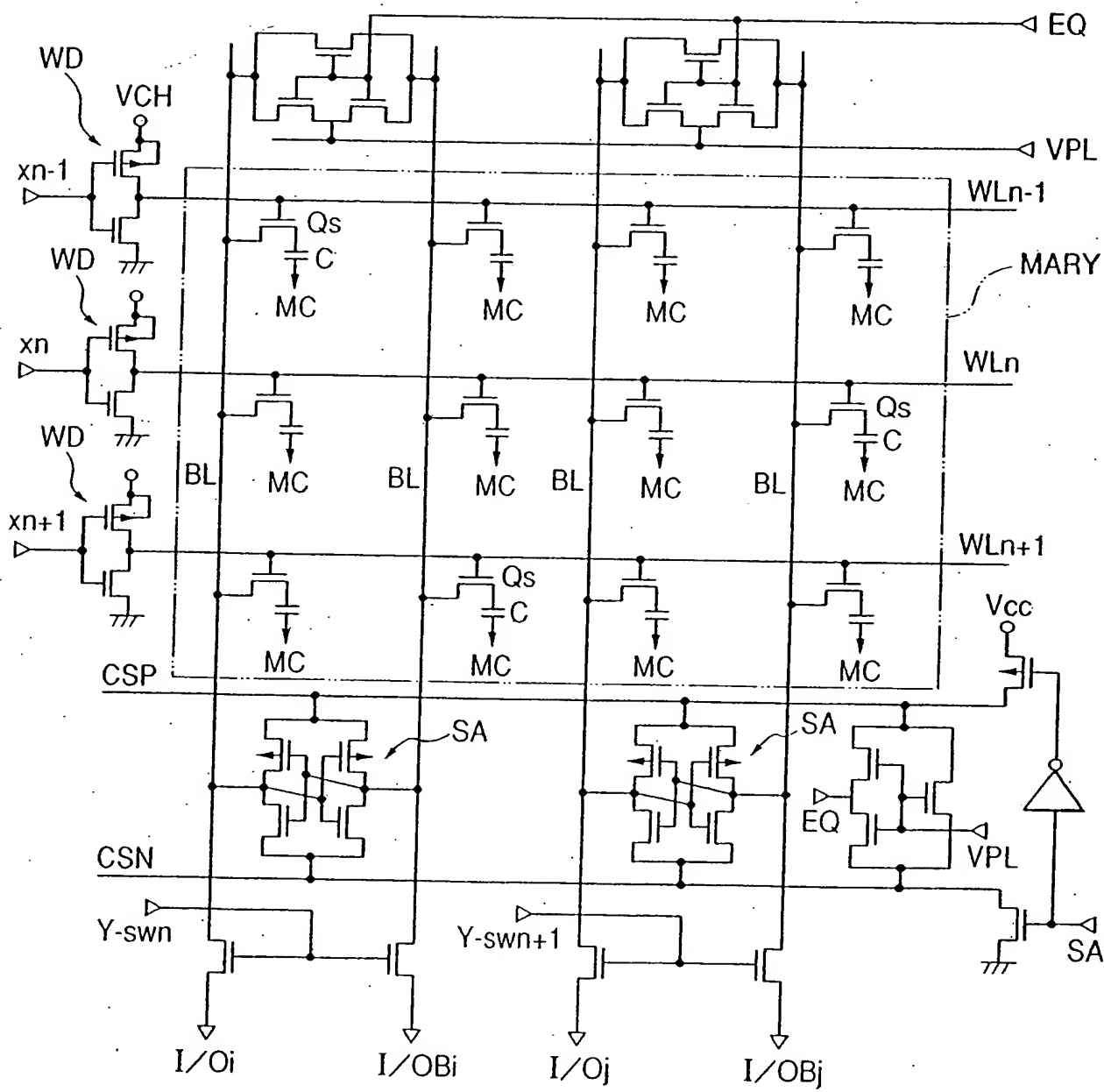


FIG. 2

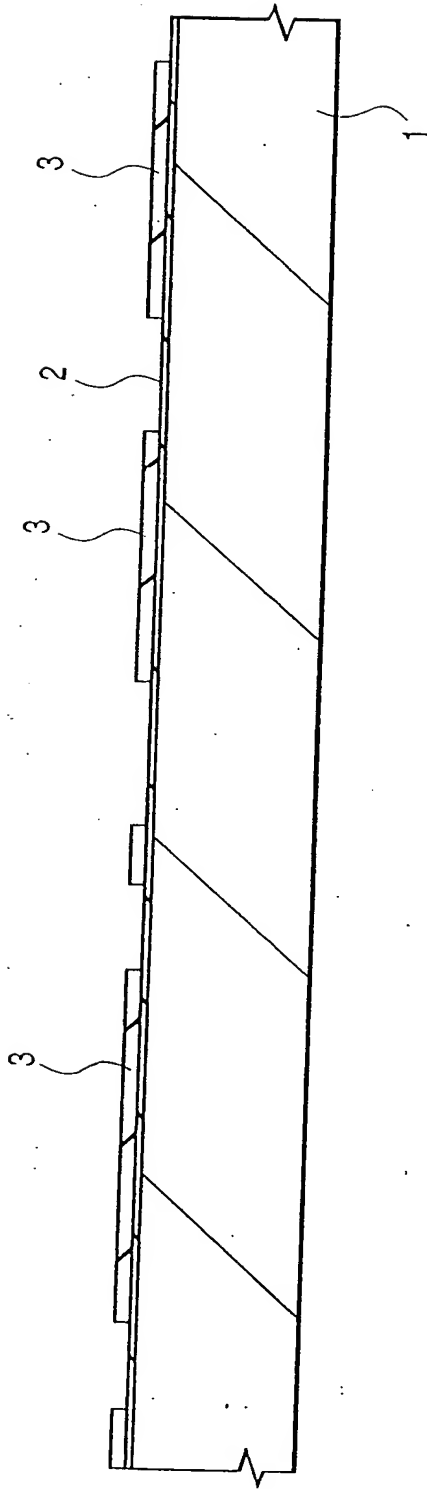


FIG. 3

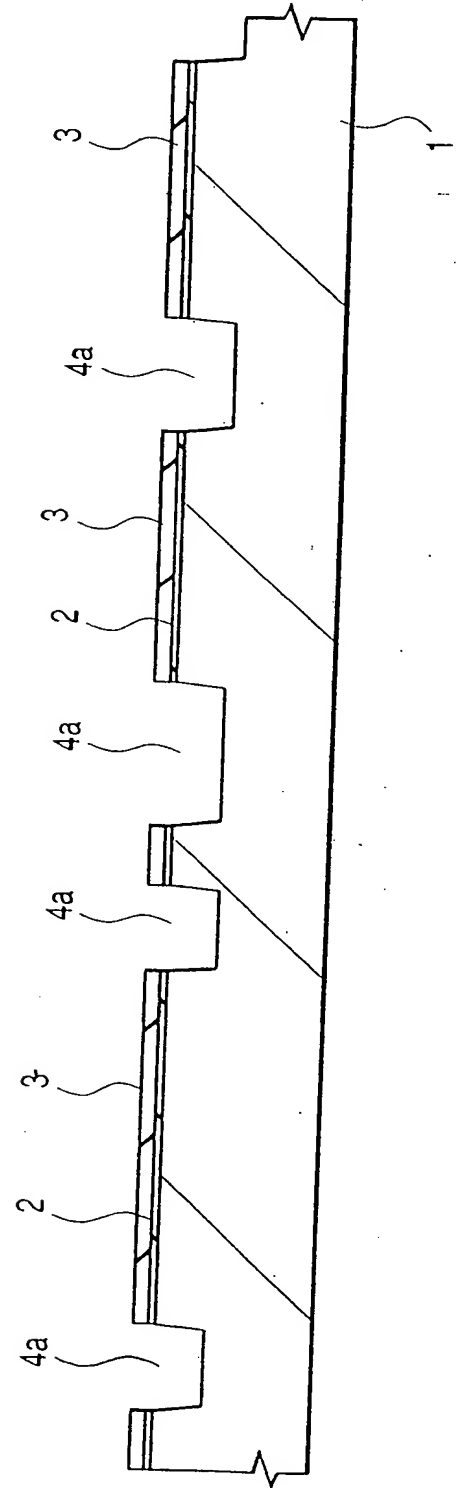


FIG. 4

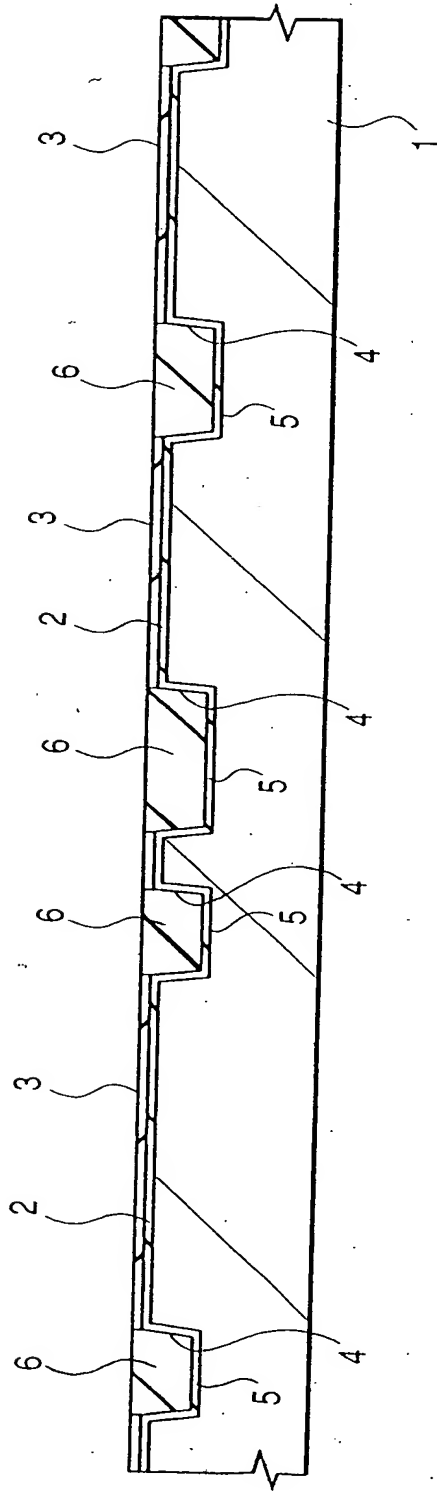


FIG. 5

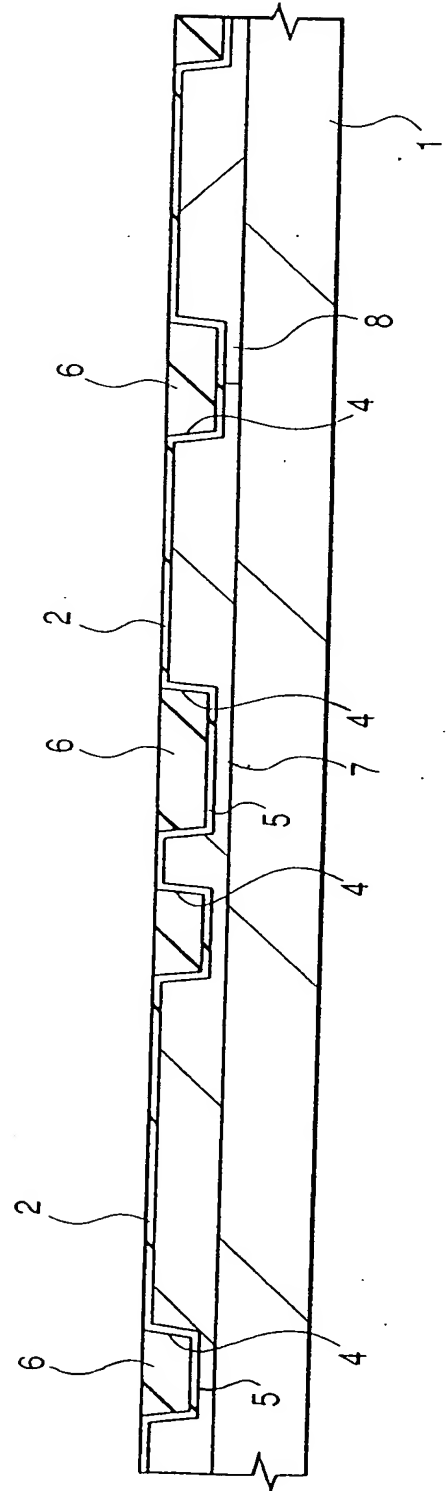


FIG. 6

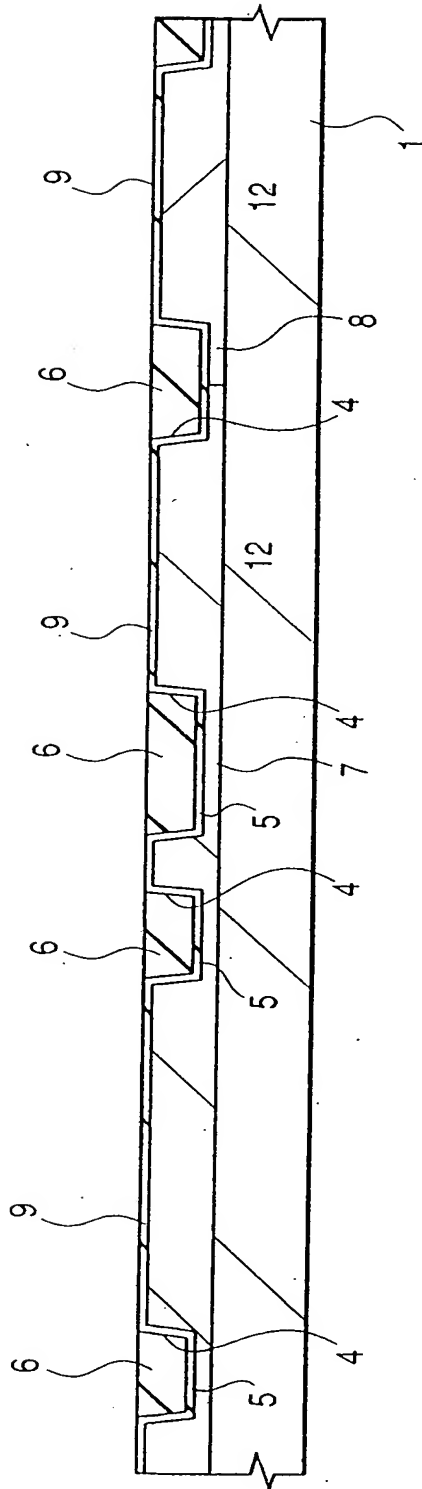
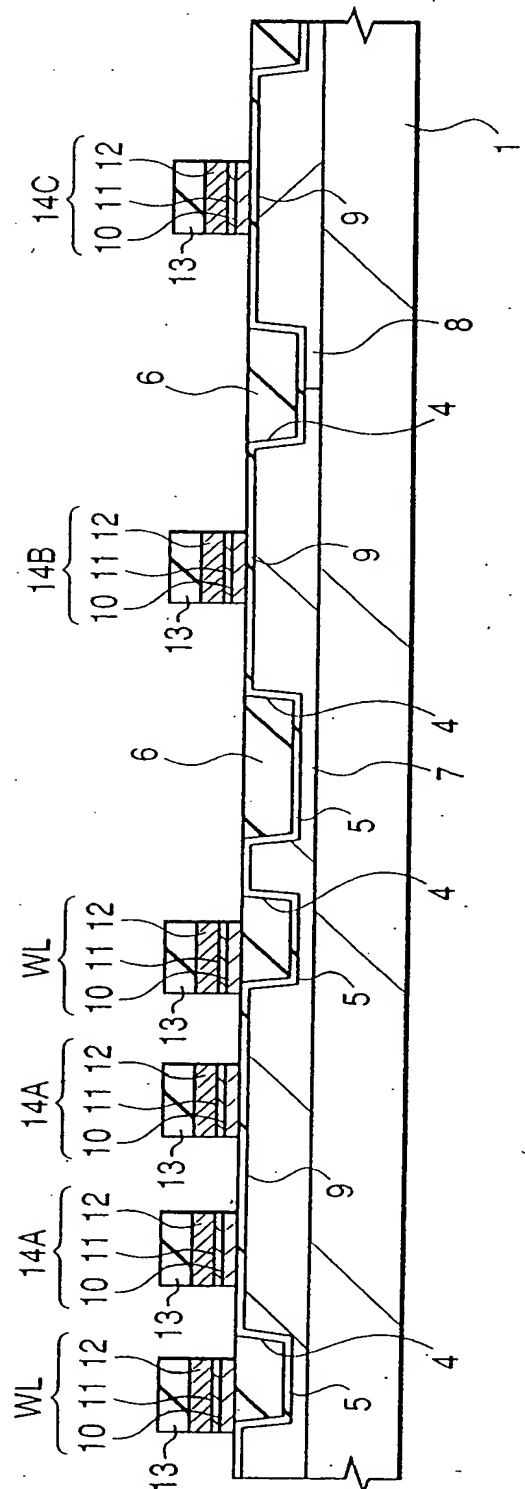


FIG. 7



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FIG. 8

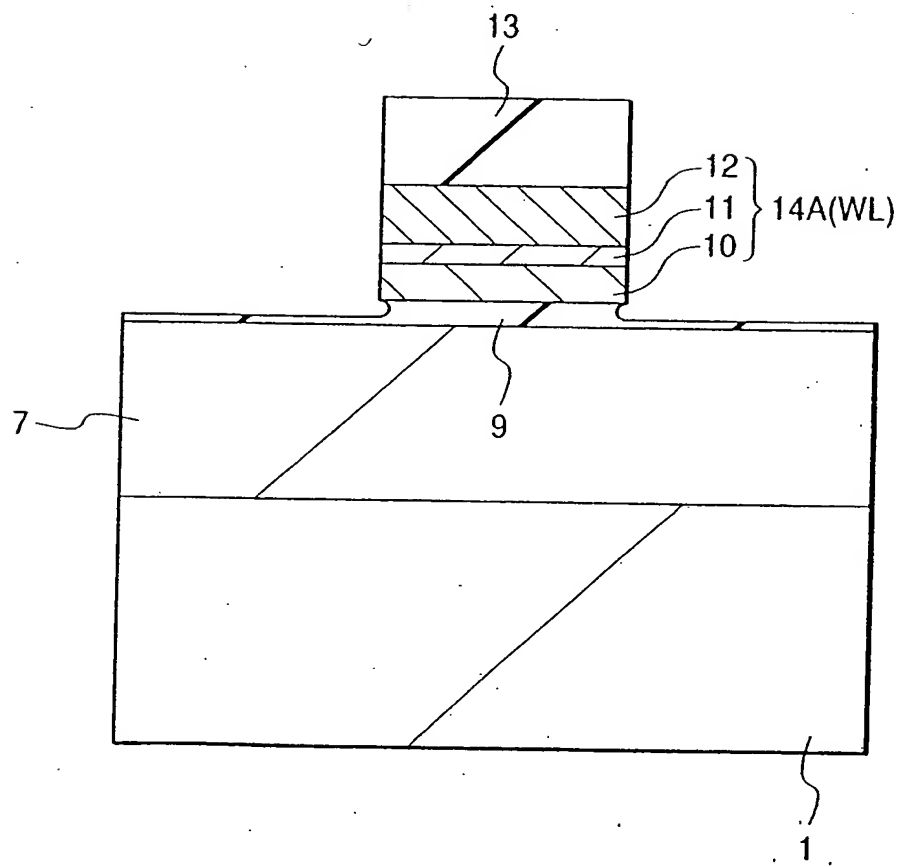


FIG. 9(a)

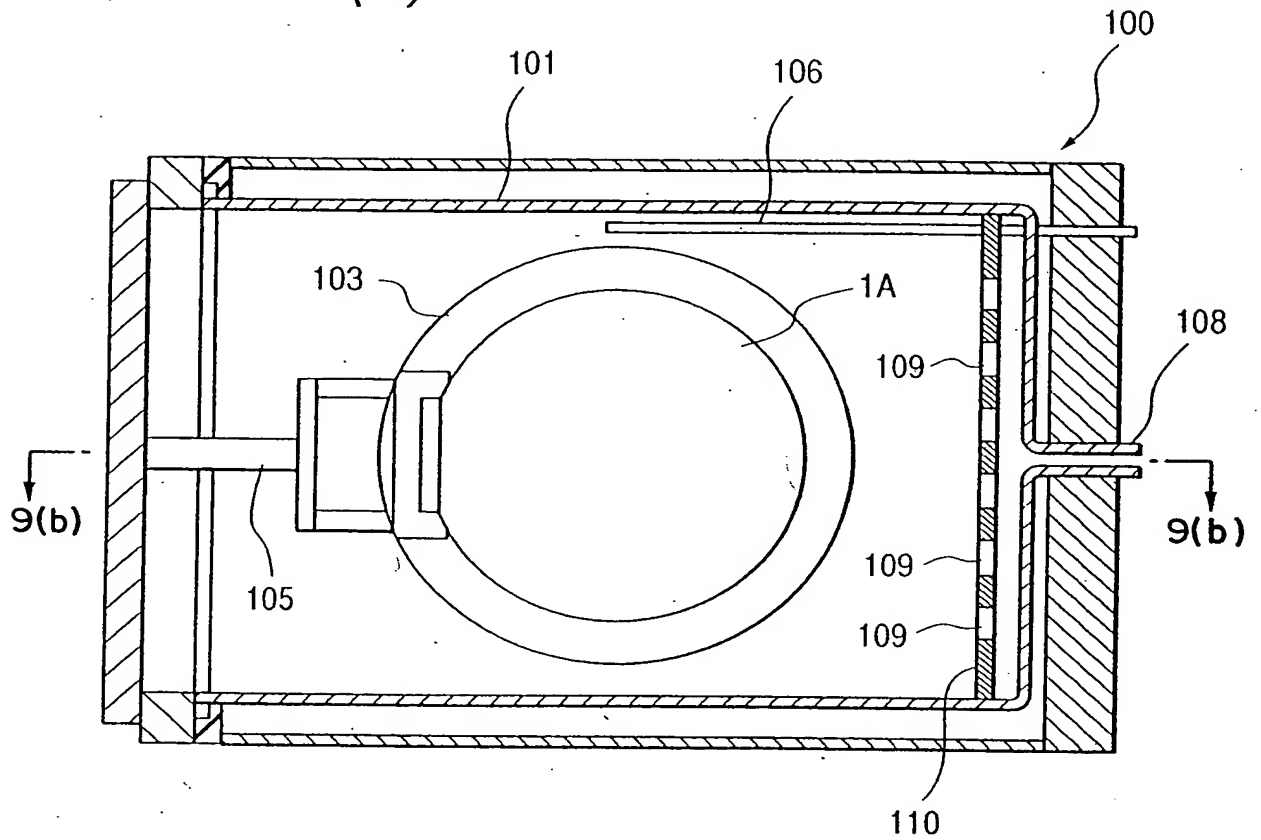


FIG. 9(b)

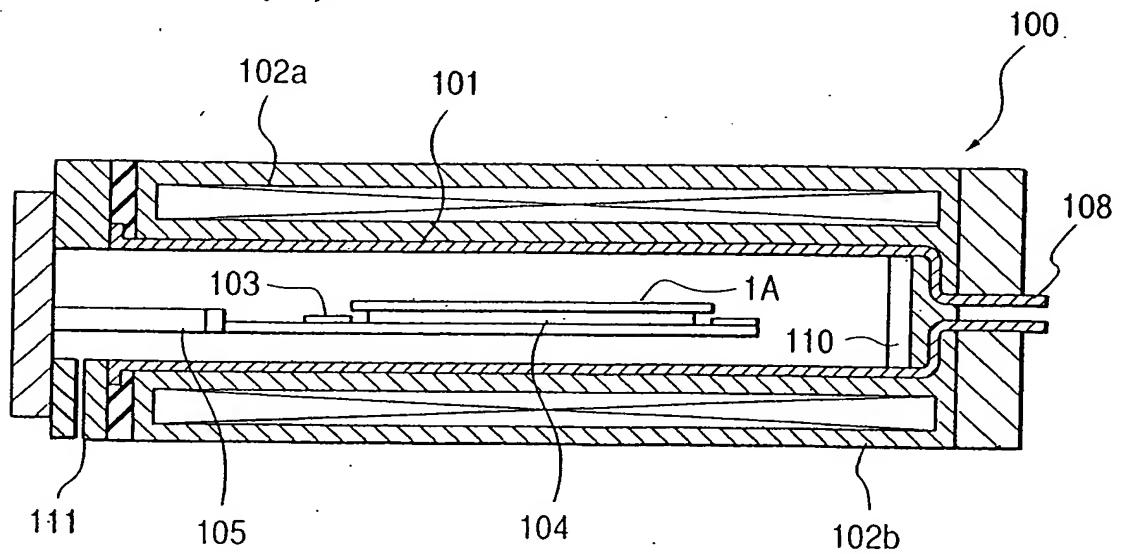


FIG. 10(a)

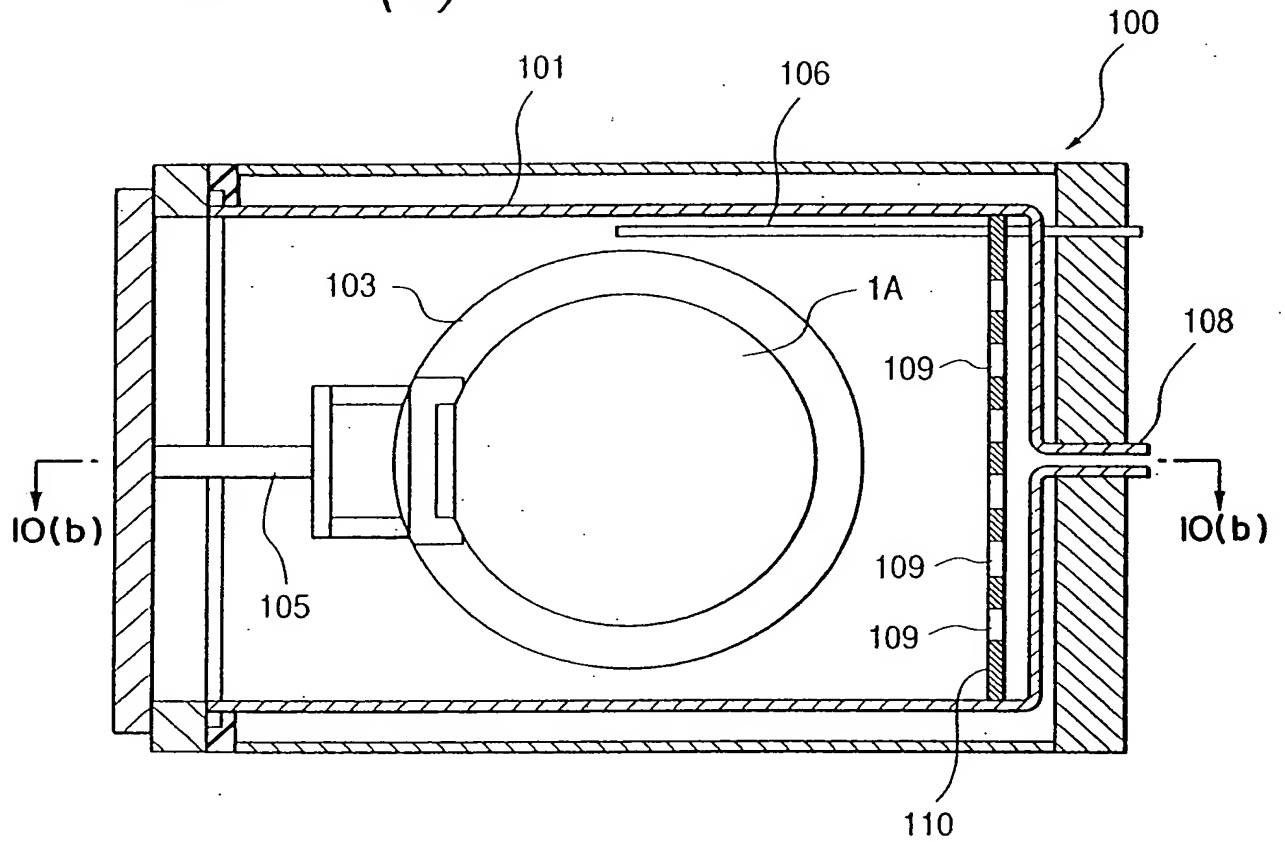


FIG. 10(b)

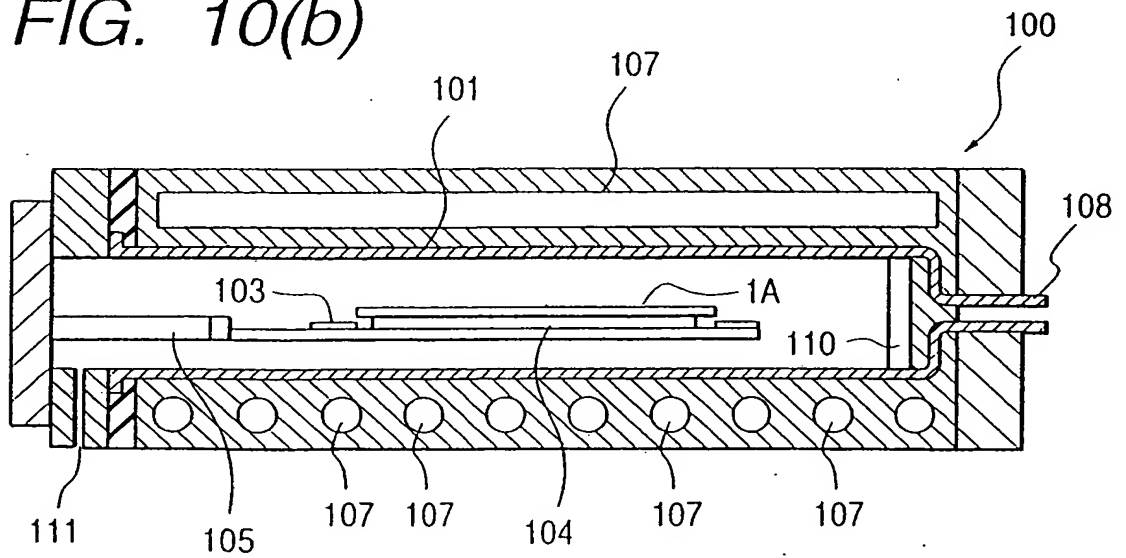
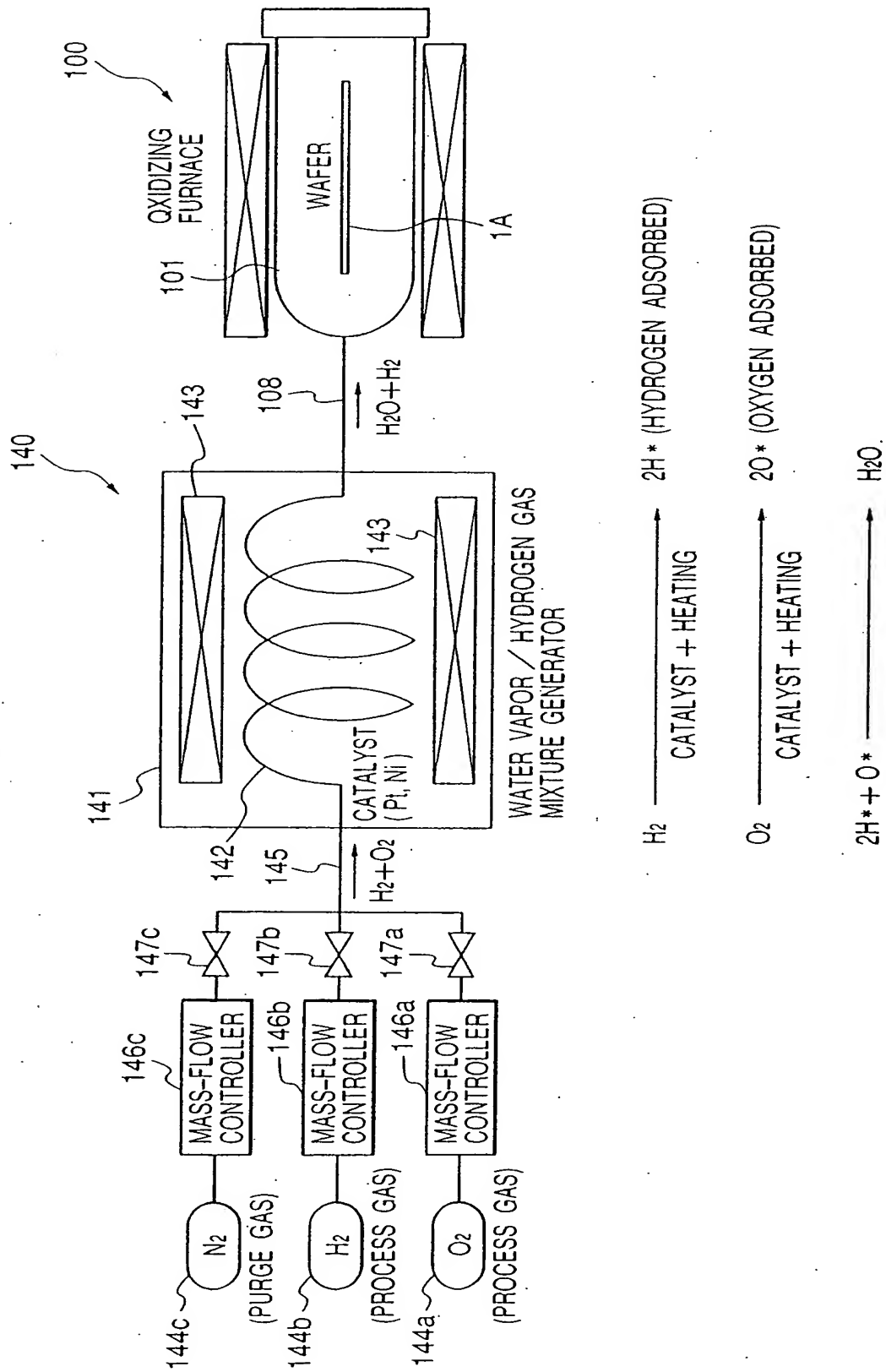


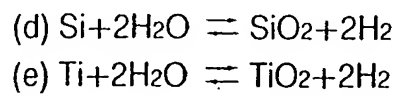
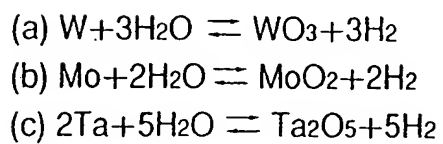
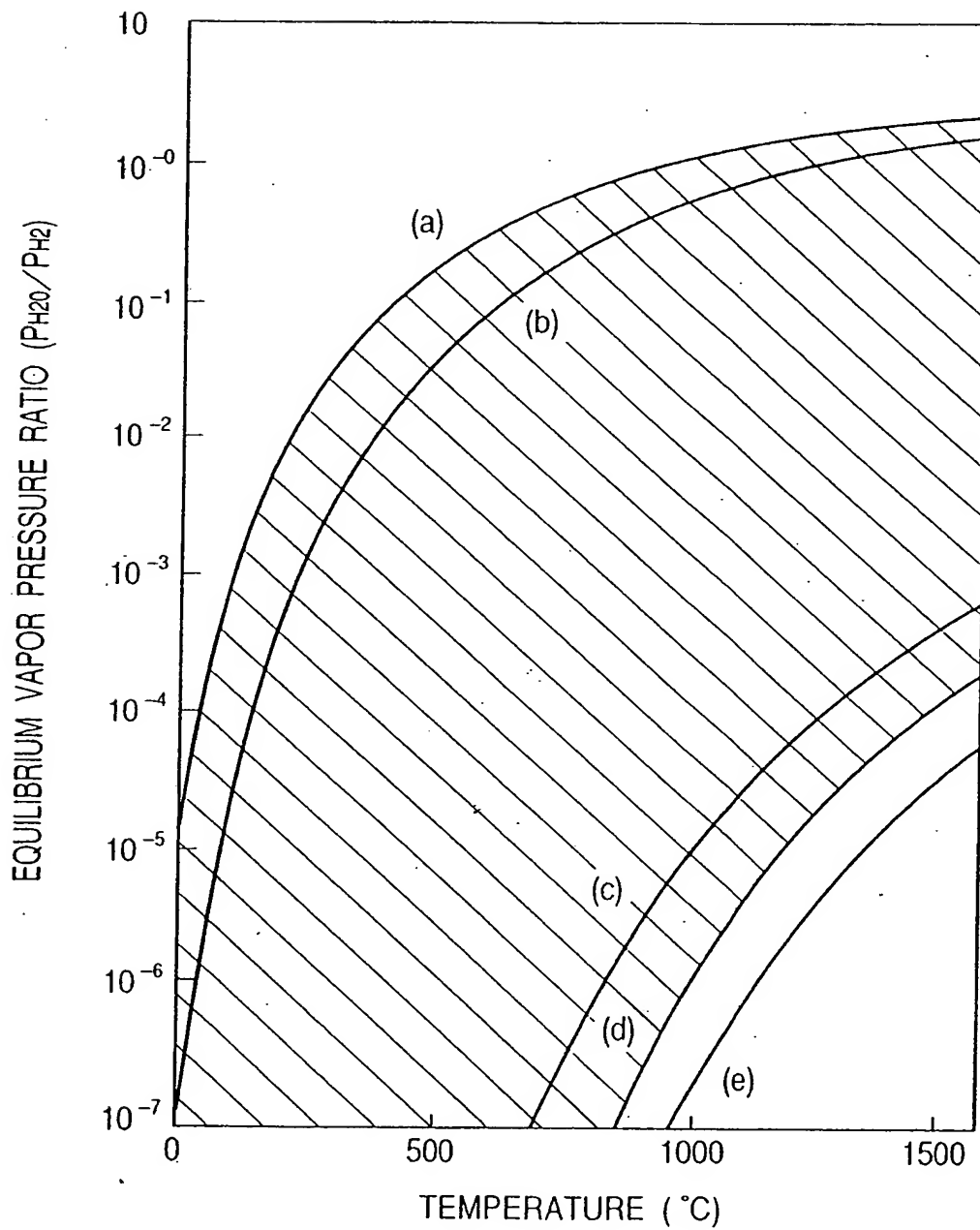


FIG. 11



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FIG. 12



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FIG. 13

EVENT	LOADING OF WAFER	PURGING WITH N <sub>2</sub>	PURGING WITH H <sub>2</sub>	INTRODUCTION OF O <sub>2</sub>	ANNEALING	AFTER- PURGE	UNLOADING OF WAFER	REMARKS
TIME	55"	30"	t <sub>1</sub>	15"	OPTIONAL (SEVERAL MIN)	2' 20"	65"	6' 10" + OPTIONAL
FLOW RATE OF N <sub>2</sub>								
FLOW RATE OF H <sub>2</sub>								
FLOW RATE OF O <sub>2</sub>								

t<sub>1</sub>: TIME NECESSARY FOR COMPLETE EXCHANGE OF N<sub>2</sub> FOR H<sub>2</sub>

FIG. 14

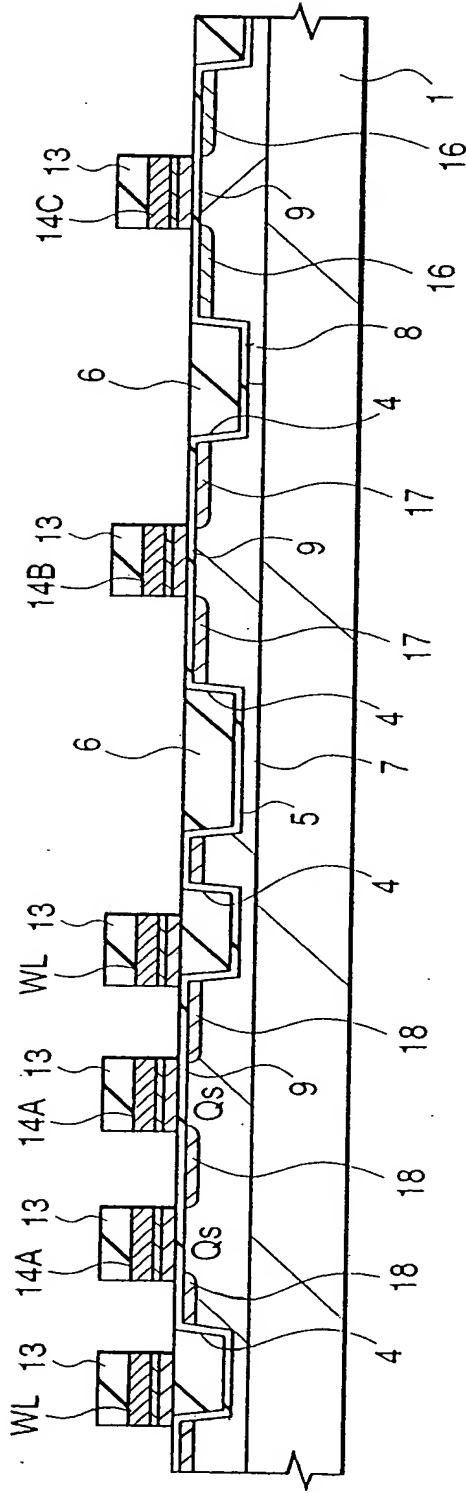


FIG. 15

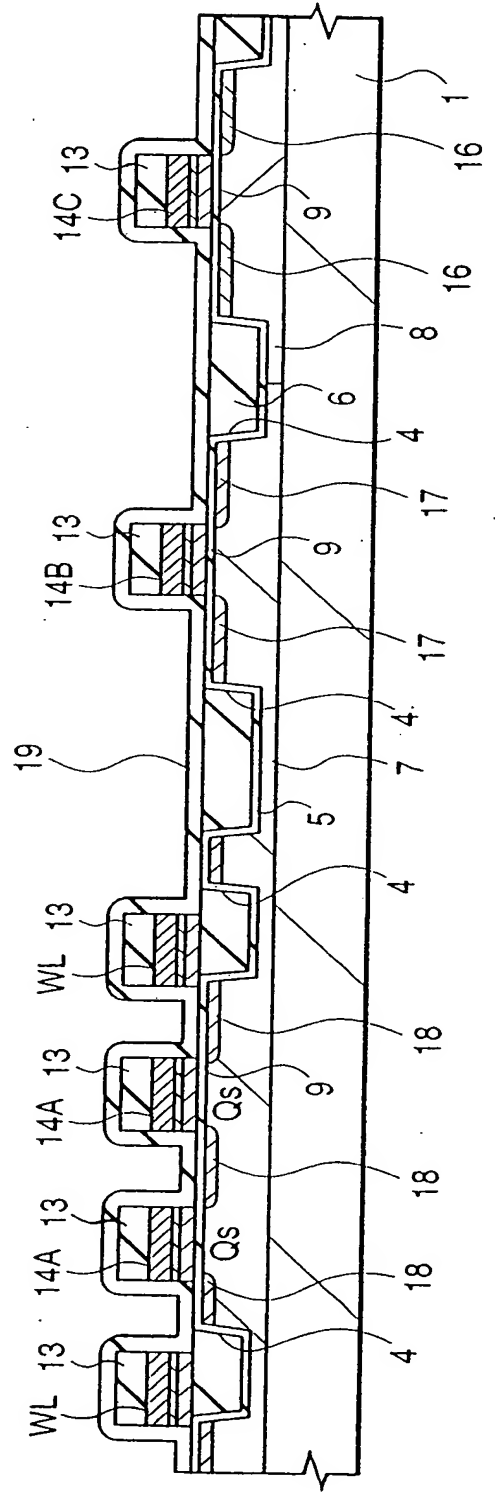


FIG. 16

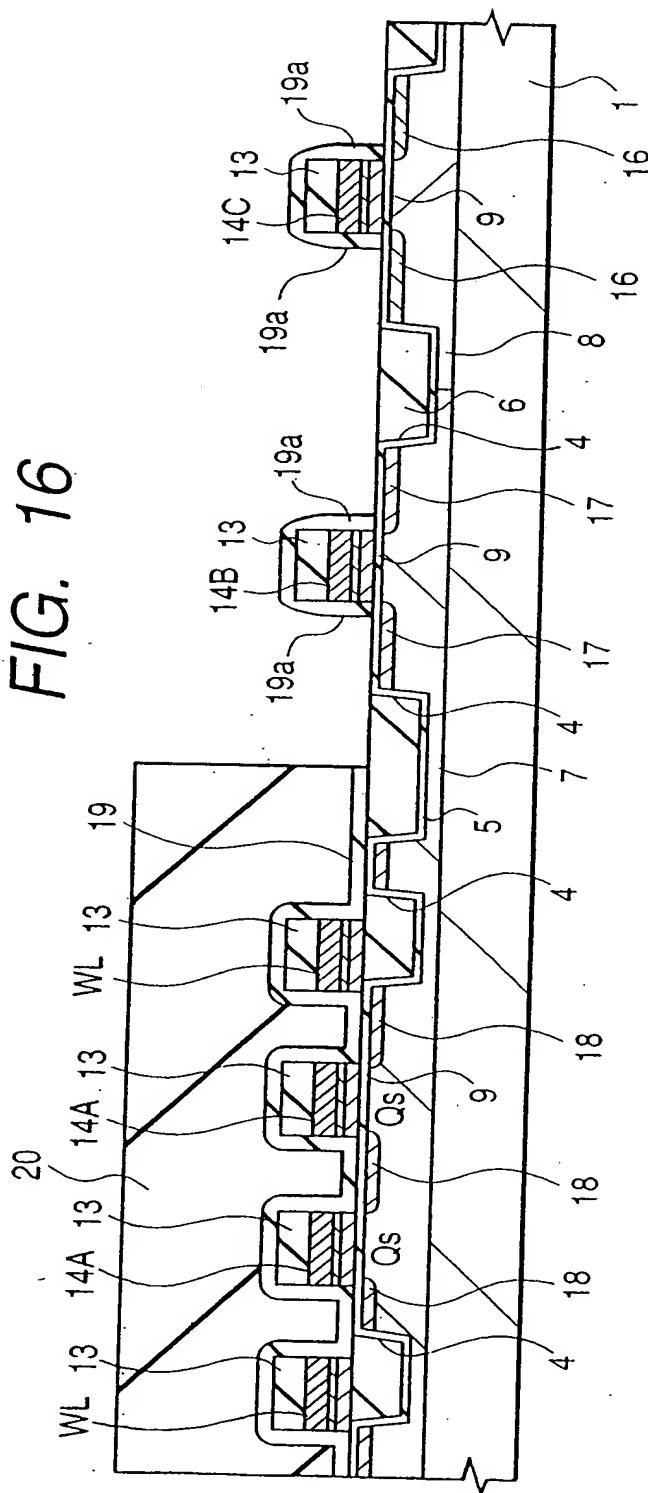


FIG. 17

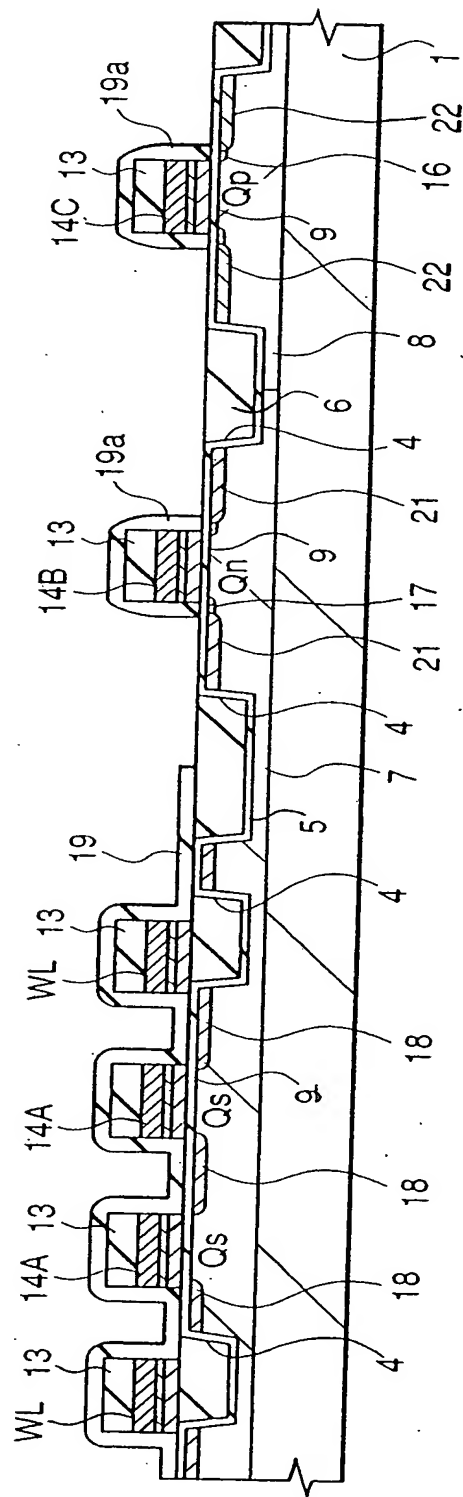




FIG. 19

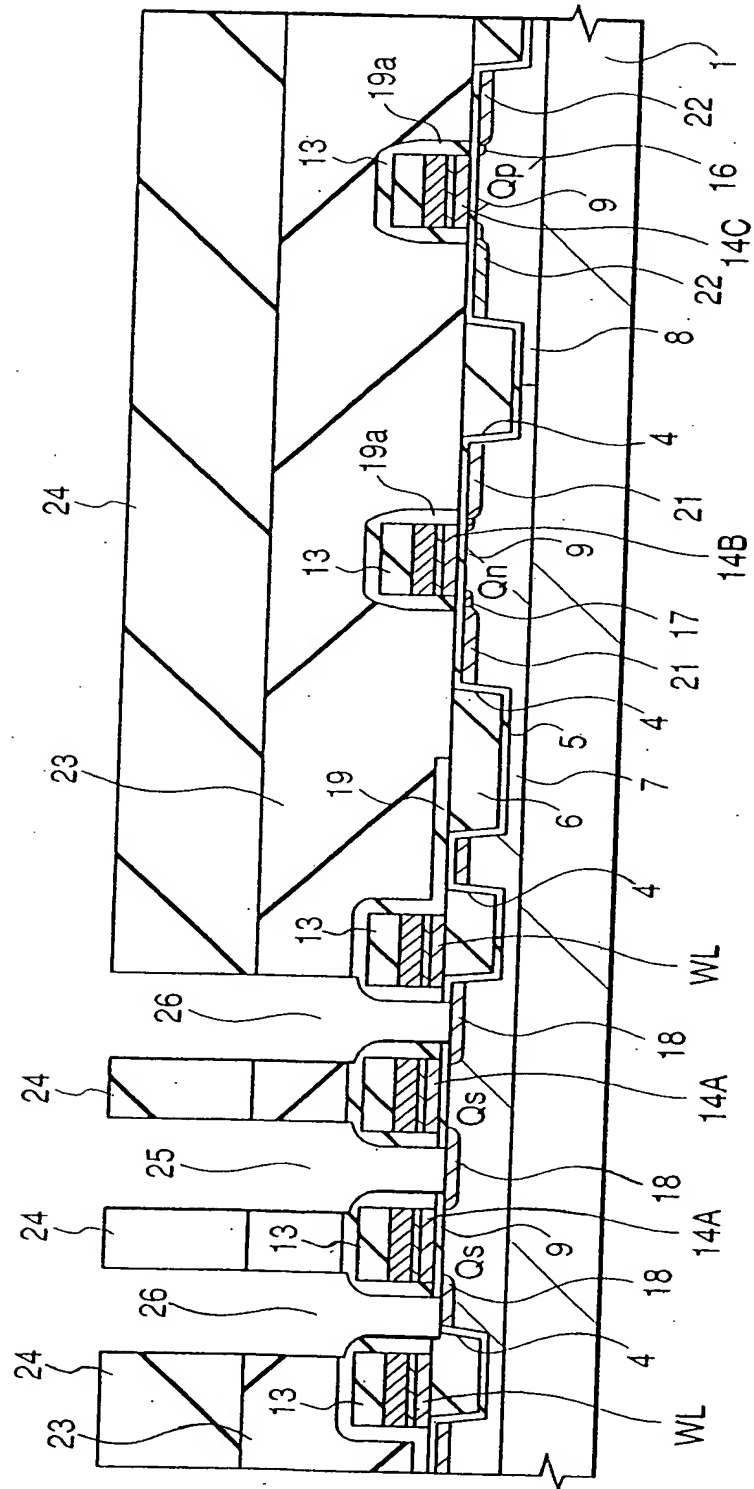


FIG. 20

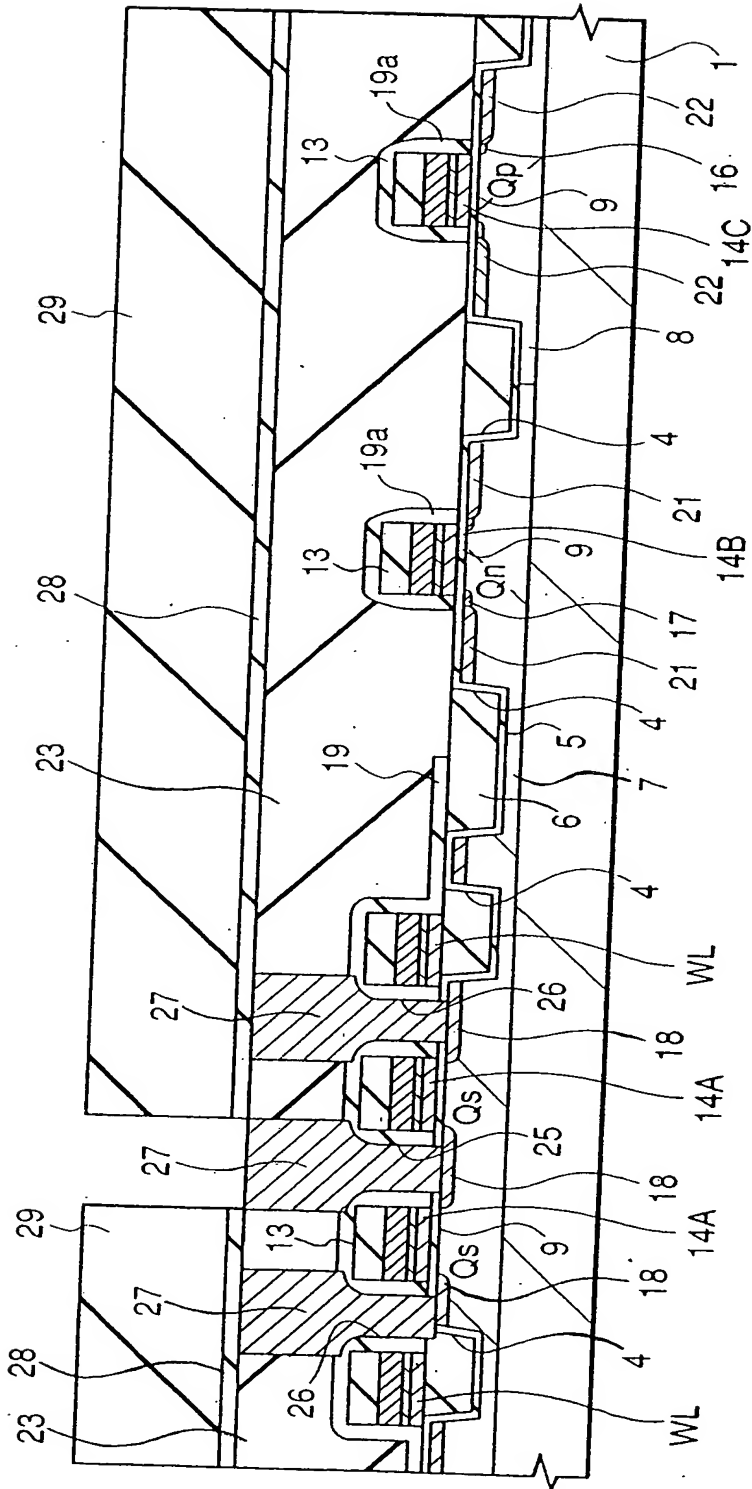




FIG. 21

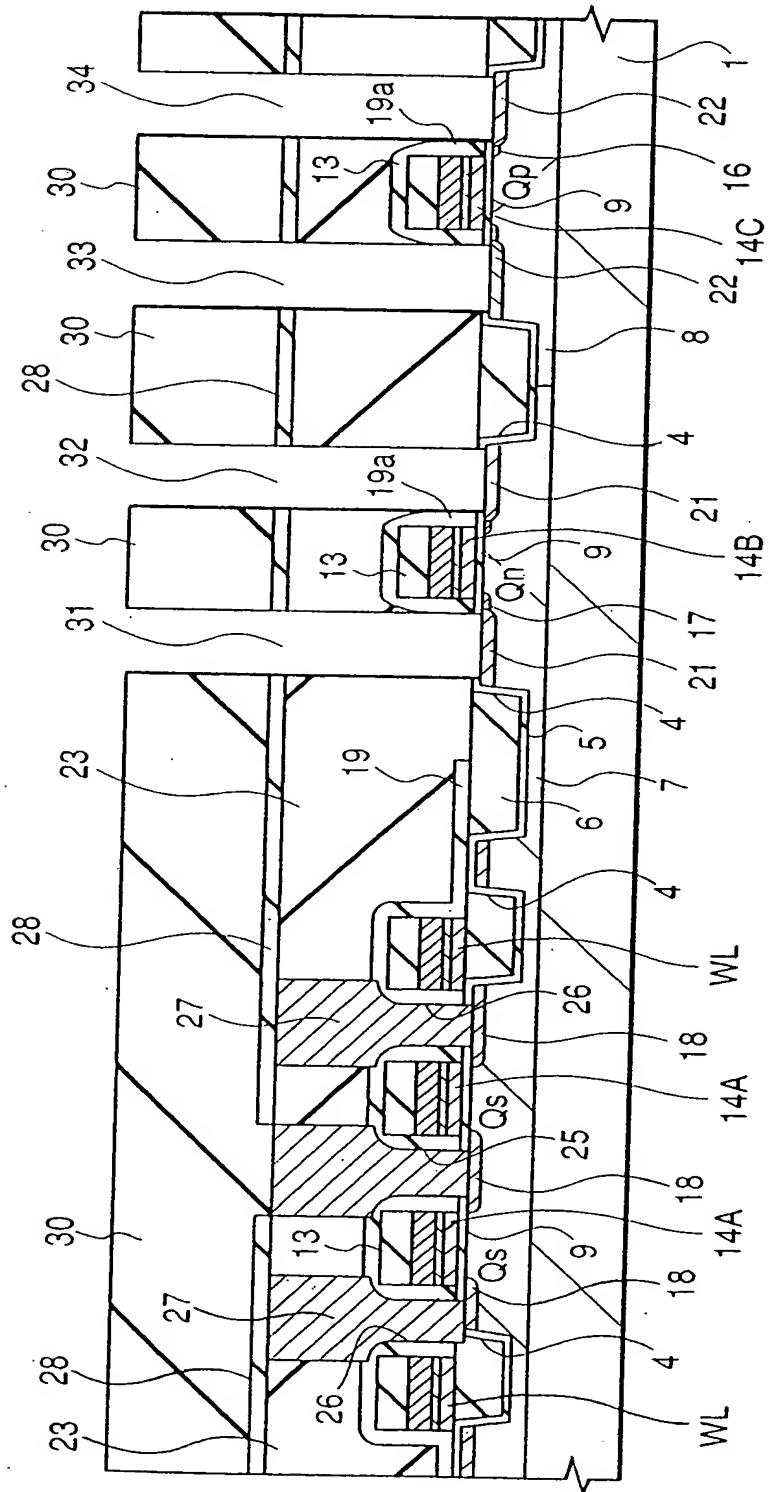


FIG. 22

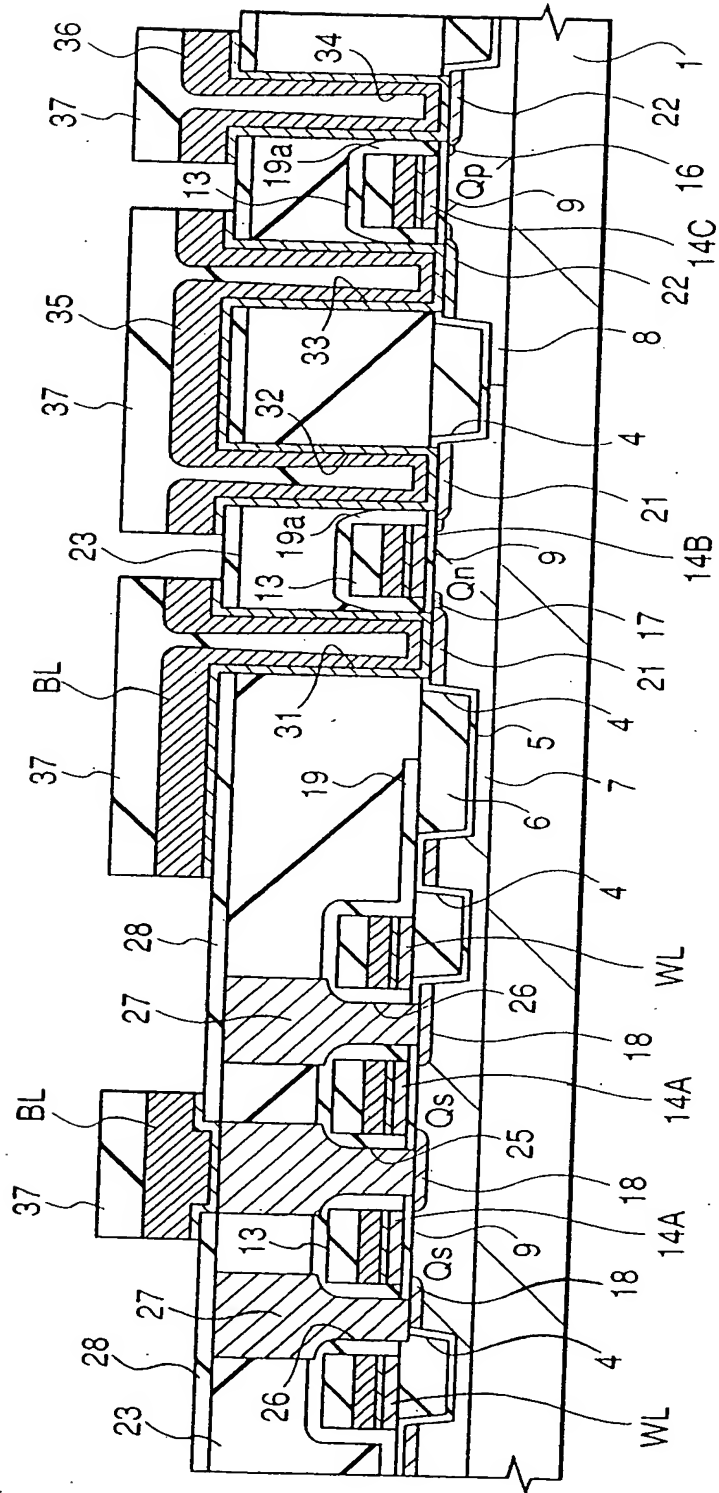


FIG. 23

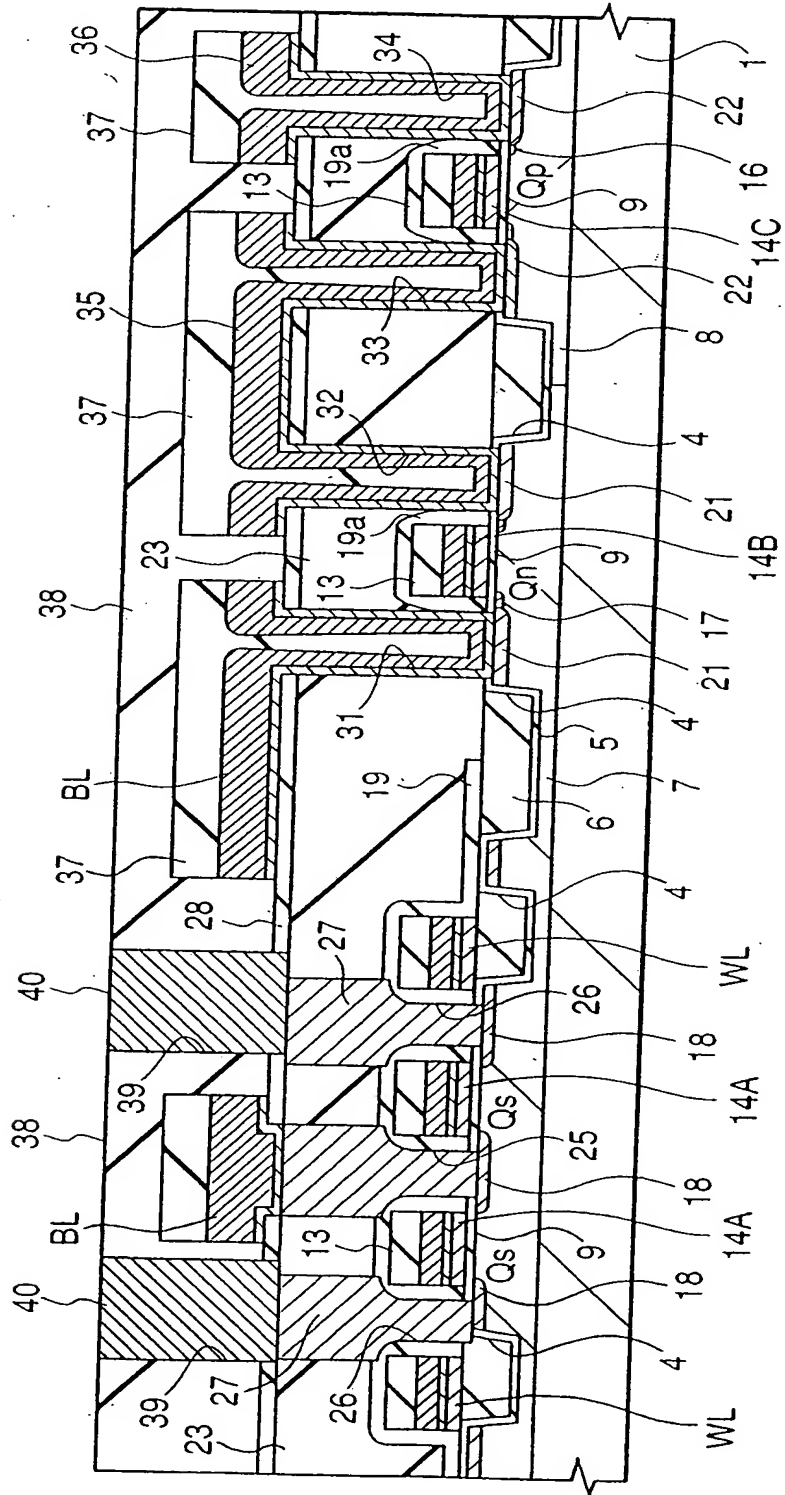




FIG. 25

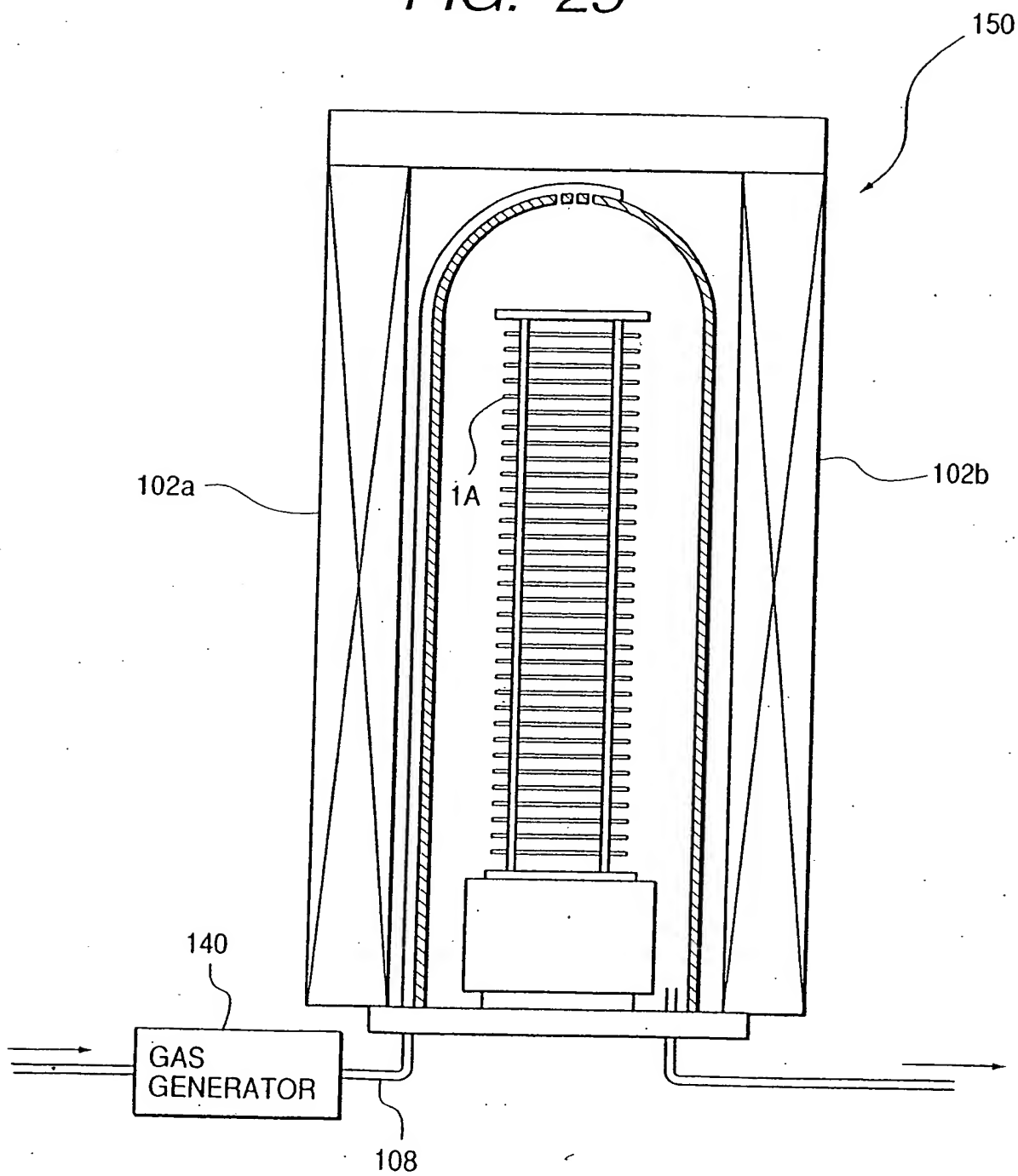


FIG. 26

EVENT	LOADING OF WAFER	PURGING WITH N <sub>2</sub>	PURGING WITH H <sub>2</sub>	INTRODUCTION OF O <sub>2</sub>	ANNEALING	AFTER- PURGE	UNLOADING OF WAFER	REMARKS
TIME	10'	1'	t <sub>2</sub>	15"	OPTIONAL (SEVERAL MIN)	2' 20"	10'	23' 40" + OPTIONAL
FLOW RATE OF N <sub>2</sub>								
FLOW RATE OF H <sub>2</sub>								
FLOW RATE OF O <sub>2</sub>								

t<sub>2</sub>: TIME NECESSARY FOR COMPLETE EXCHANGE OF N<sub>2</sub> FOR H<sub>2</sub>